

The logo for Biju Patnaik University of Technology consists of several overlapping circles in blue, black, and yellow.

**Biju Patnaik University of  
Technology, Odisha B.E./B.Tech EE  
Sem 3 syllabus**

## **Analog Electronic Circuits**

### **REC3C001 Analog Electronic Circuits**

**MODULE - I MOS Field-Effect Transistor:** Principle and Operation of FETs and MOSFETs; P-Channel and N- Channel MOSFET; Complimentary MOS; V-I Characteristics of E- MOSFET and D-MOSFET; MOSFET as an Amplifier and as a Switch. Biasing of BJT: Load lines (AC and DC); Operating Points; Fixed Bias and Self Bias, DC Bias

with Voltage Feedback; Bias Stabilization; Examples. Biasing of FETs and MOSFETs: Fixed Bias Configuration and Self Bias Configuration, Voltage Divider Bias and Design

**MODULE - II Small Signal Analysis of BJTs:** Small-Signal Equivalent-Circuit Models; Small Signal Analysis of CE, CC, CB amplifiers. Effects of  $R_S$  and  $R_L$  on CE amplifier operation, Emitter Follower; Cascade amplifier, Darlington Connection and Current Mirror Circuits. Small Signal Analysis of FETs: Small-Signal Equivalent-Circuit Model, Small Signal Analysis of CS, CD, CG Amplifiers. Effects of  $R_{SIG}$  and  $R_L$  on CS Amplifier; Source Follower and Cascaded System.

**MODULE - III High Frequency Response of FETs and BJTs:** High Frequency equivalent models and frequency Response of BJTs and FETs; Frequency Response of CS Amplifier, Frequency Response of CE Amplifier.

**MODULE - IV Feedback amplifier and Oscillators:** Concepts of negative and positive feedback; Four Basic Feedback Topologies, Practical Feedback Circuits, Principle of Sinusoidal Oscillator, Wein-Bridge, Phase Shift and Crystal Oscillator Circuits, Power Amplifier (Class A, B, AB, C).

**MODULE - V Operational Amplifier:** Ideal Op-Amp, Differential Amplifier, Op-Amp Parameters, Non-inverting Configurations, Open-loop and Closed-loop Gains, Differentiator and Integrator, Instrumentation amplifier.

**Books:**

- Microelectronics Circuits, Adel Sedra and Kenneth C Smith, Oxford University Press, New Delhi, 5th Edition, International Student Edition, 2009. (Selected portion of Chapter 2, 4, 5, 6, 8, 13, and 14)
- Electronic Devices and Circuits theory, R.L. Boylestad and L. Nashelsky, Pearson Education, New Delhi, 9th/10th Edition, 2013. (Selected portions of Chapter 4, 5, 6, 7, 8, 9, 10, 11, 12, and 14)
- Milliman's Electronics Devices and Circuits, J. Milliman, C. Halkias, S. Jit., Tata McGraw Hill
- Electronic Devices and Circuits, Jimmie J. Cathey adapted by Ajay Kumar Singh, Tata McGraw Hill Publishing Company Ltd., New Delhi, 3rd Edition, (For Problem Solving)
- Electronics Circuits Analysis and Design, Donald A. Neamen, Tata McGraw Hill Publishing Company Ltd., New Delhi, 3rd Edition, 2002.
- Integrated Electronics: Analog and Digital Circuits and Systems, J. Milliman, C. Halkias, Tata McGraw Hill Publishing Company Ltd., New Delhi, 2nd Edition, 2004.
- Microelectronic Circuits: Analysis and Design, M.H. Rashid, PWS Publishing Company, a division of Thomson Learning Inc. India Edition.
- Electronic device and circuits, David A. Bell, Oxford University Press, 5th edition, 2008.
- Electronics devices and circuits, Anil.K.Maini, Wiley India Pvt.Ltd, 2009